Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
M.Tech. in Embedded System
SEMESTER - III (2021-2023)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK L	T	P	CREDITS	THEORY End Sem Unversity Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	PRACTICAL End Sem Unversity Exam (60%)	Teachers Assessment* (40%)	TOTAL MARKS
1	-	Elective III	3	0	0	3	60	20	20	0	0	100
2	-	Elective IV	3	0	0	3	60	20	20	0	0	100
3	-	MOOCs (Generic)	2	0	0	2	-	-	-	-	-	100
4	MTES301	Dissertation Phase I	0	0	20	10	0	0	0	180	120	300
		TOTAL	8	0	20	18	120	40	40	180	120	600

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

* Students have to compulsorily opt MOOCS course of two credit from Swayam Portal

Elective III		
S. No.	Course Code	Course Name
1	MTES213	Machine Learning
2	MTDC214	Artificial Intelligence
3	MTES233	Hardware/Software Codesign

Elective IV		
S. No.	Course Code	Course Name
1	MTES214	Sensors and Actuators
2	MTES224	Internet of Things
3	MTES234	Wireless Sensor Networks

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examination
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore